

<b>Notice of Allowability</b>	Application No.	Applicant(s)
	10/675,942	LIN, I-MING
	Examiner Dennis-Doon Chow	Art Unit 2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to 10/2/03.
  2.  The allowed claim(s) is/are 1-10.
  3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
    - a)  All      b)  Some\*      c)  None      of the:
      1.  Certified copies of the priority documents have been received.
      2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
      3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- \* Certified copies not received: \_\_\_\_\_.
- Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**
4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
  5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
    - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
      - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
    - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
  6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

- |  |  |
|--|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)            |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                 | 6. <input type="checkbox"/> Interview Summary (PTO-413),<br>Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),<br>Paper No./Mail Date _____. | 7. <input type="checkbox"/> Examiner's Amendment/Comment                               |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit<br>of Biological Material           | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance   |
|  | 9. <input type="checkbox"/> Other _____.   |

## REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance:

The present invention relates to a keyboard control apparatus with a USB interface for use in a notebook computer having a number of keys. In one embodiment, a mapping unit is employed to store a number of key matrix codes corresponding to the keys. In accordance with a clock signal and a start signal, a shift register is capable of generating a reference signal and a number of scan output signals. A key matrix circuit then receives the scan output signals to provide a number of scan input signals. An address generator is used to generate an address signal based on the reference signal and a triggered scan input signal. Thus, a selector can select one key matrix code designated to the address signal from the mapping unit. Additionally, an interface converter further converts the selected key matrix code into a pair of differential signals compliant with the USB specification. The prior art of record does not teach or suggest a *shift register for generating a reference signal and a plurality of scan output signals in accordance with a clock signal and a start signal; a key matrix circuit coupled to said shift register to receive said plurality of scan output signals, for outputting a plurality of scan input signals and triggering one of said plurality of scan input signals when a corresponding key of said notebook computer is actuated, in which said plurality of keys are rendered by means of said plurality of scan output signals together with said plurality of scan input signals; an address generator coupled to said shift register to receive said reference signal and coupled to said key matrix circuit to receive said plurality of scan input signals, for generating an address signal in accordance with said*

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*reference signal and said triggered scan input signal; a selector separately coupled to said address generator and said mapping unit, for receiving said address signal and selecting one of said plurality of key matrix codes which is designated to said address signal; and an interface converter coupled to said selector, for converting said selected key matrix code into a pair of differential signals compliant with the universal serial bus (USB) specification.*

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

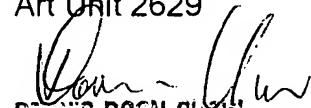
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dennis-Doon Chow whose telephone number is 571-272-7767. The examiner can normally be reached on 8:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on 571-272-3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Dennis-Doon Chow  
Primary Examiner  
Art Unit 2629

  
DENNIS-DOON CHOW  
PRIMARY EXAMINER

D. Chow  
April 11, 2006